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Al Research at Scale -Opportunities on the Road Ahead

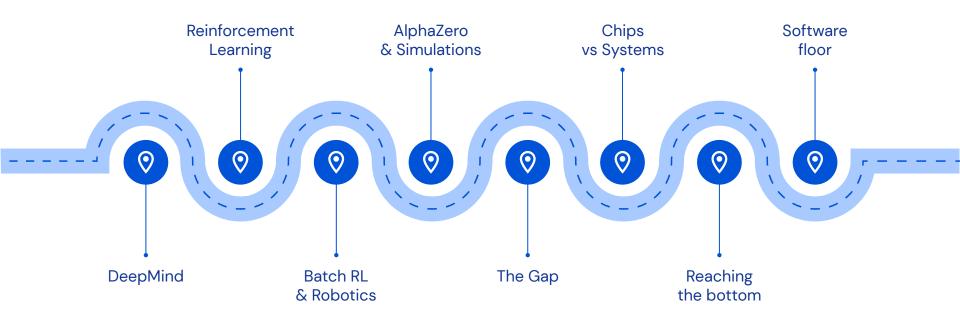
Dan Belov

Digression: Who am I?



Evan-Amos / Public domain

Roadmap





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An Apollo Program for Al

OUR MISSION



01

Use it to solve everything else

Neuroscience Inspired Artificial Intelligence

Neuroscience has made fundamental contributions to **advancing Al research**.

Past contributions have rarely involved a simple transfer of full-fledged solutions

Rather, neuroscience has typically been useful in a **subtler way**

Neuroscience-Inspired Artificial Intelligence

Demis Hassabis,^{1,2,*} Dharshan Kumaran,^{1,3} Christopher Summerfield,^{1,4} and Matthew Botvinick^{1,2} ¹DeepMind, 5 New Street Square, London, UK ²Gatsby Computational Neuroscience Unit, 25 Howland Street, London, UK ³Institute of Cognitive Neuroscience, University College London, 17 Queen Square, London, UK ⁴Department of Experimental Psychology, University of Oxford, Oxford, UK *Correspondence: dhcontact@google.com http://dx.doi.org/10.1016/j.neuron.2017.06.011 Neocortex Hippocampus в А *Slow acquisition of structure *Rapid storage: individual experiences *Parametric *Non-parametric instance-based system *Efficient representations for *Sparse non-overlapping representations Glimpse Sensor generalisation (poor generalization) **Episodic Control** LSTM Central Executive С honologica Visuospatial Episodic Sketchpad Buffer Loop **Cascade Model** EWC D Controlle W1A* External memory Task B



Virtual environments accelerate AI but carry complexity



Games are a proving ground for real-world situations

Stimulate intelligence

By presenting a diverse set of challenges

Good to test in simulations

Efficient, run thousands in parallel, faster than real time

Measure progress and performance

Measure progress and compare against human performance

Virtual environments are rich substrates to progress Al Research. But their increasing complexity demands commensurate technology infrastructure



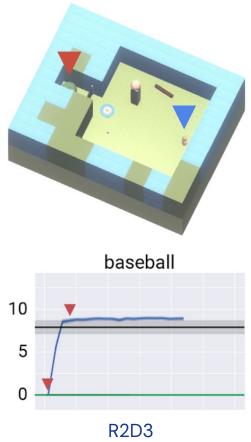
Atari with deep RL



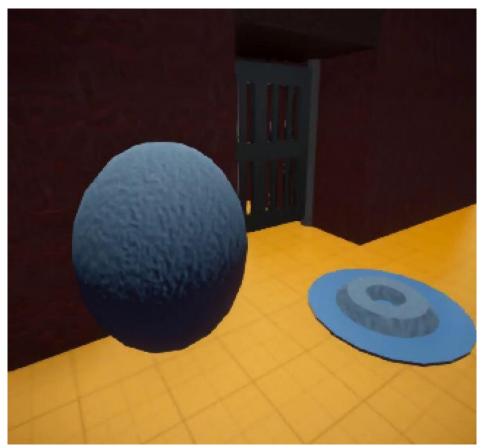
Complex planning in 3D environments







R2D3 Tom Paine, Caglar Gulcehre et al 2019





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How does this work?



Machine learning

- 1 ML is about inferring knowledge from observations or experiences, subject to the physical laws of the world.
- 2 ML is also about using this knowledge to guide observation and hypothesis testing.
- 3 ML is about creating new knowledge, using the present knowledge, to solve a large diversity of novel problems.

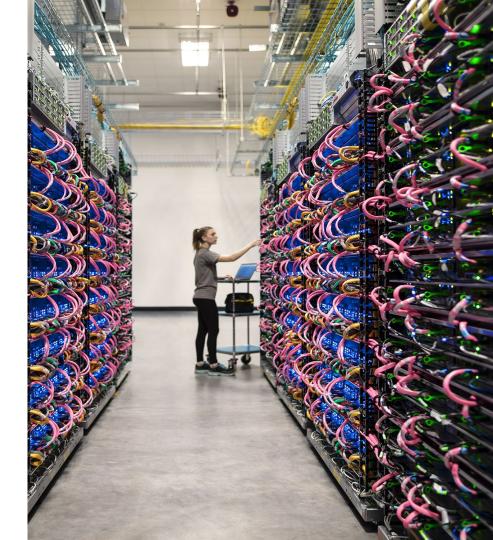


ML Magic

 Largest possible network with few general inductive biases.

Massive curated dataset. Clear goal.

Best existing communication, memory and computation infrastructure.



Supervised Deep Learning

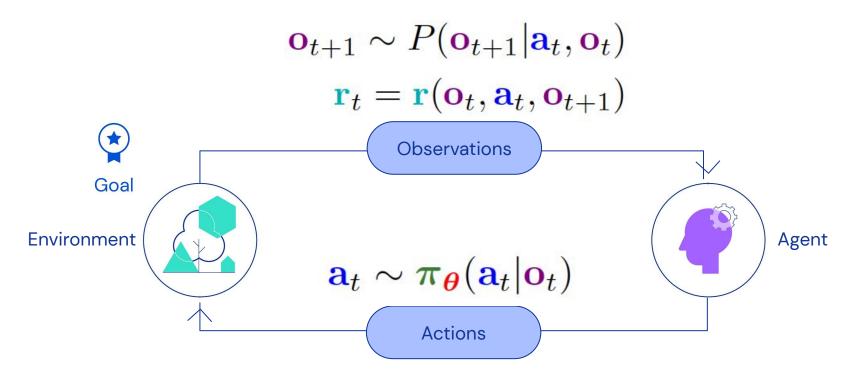
Inferring knowledge from observations.

Given pairs {inputs, outputs} can now learn pretty much any function, subject to:

- o enough data
- enough compute
- representative samples

Iron law of Deep Learning: More is More.

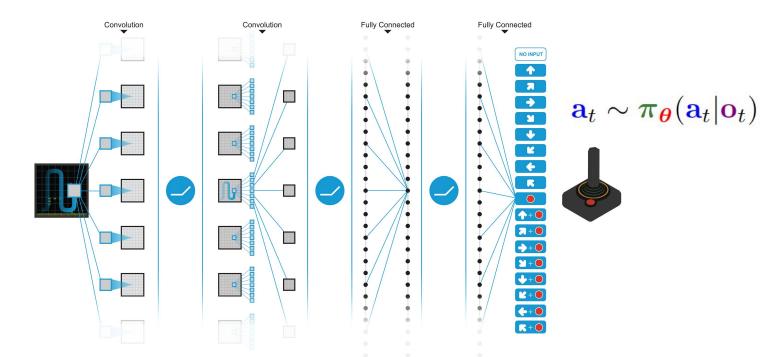
Reinforcement learning



Making good decisions by learning from experience



Deep RL uses deep neural nets as policies



Making good decisions by learning from experience



Value functions and optimal value functions

The value of being in a state and following a deterministic policy $\mathbf{a}_t = \pi(\mathbf{o}_t)$

$$\mathbf{V}^{\boldsymbol{\pi}}(\mathbf{o}_0) = \mathbb{E}_{\tau} \left[\sum_{t=0}^{\infty} \gamma^t \mathbf{r}(\mathbf{o}_t, \boldsymbol{\pi}(\mathbf{o}_t), \mathbf{o}_{t+1}) \middle| \mathbf{o}_0 \right]$$

The optimal value function

$$\mathbf{V}^{\star}(\mathbf{o}_{0}) = \max_{\boldsymbol{\pi}} \mathbb{E}_{\tau} \left[\sum_{t=0}^{\infty} \gamma^{t} \mathbf{r}(\mathbf{o}_{t}, \boldsymbol{\pi}(\mathbf{o}_{t}), \mathbf{o}_{t+1}) \middle| \mathbf{o}_{0} \right]$$

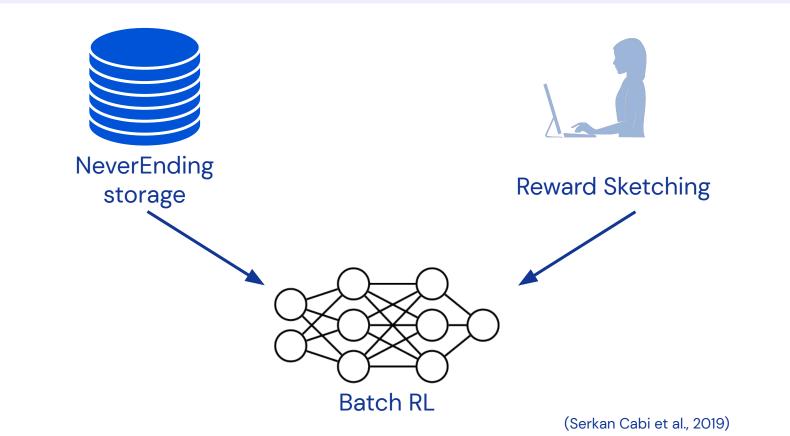


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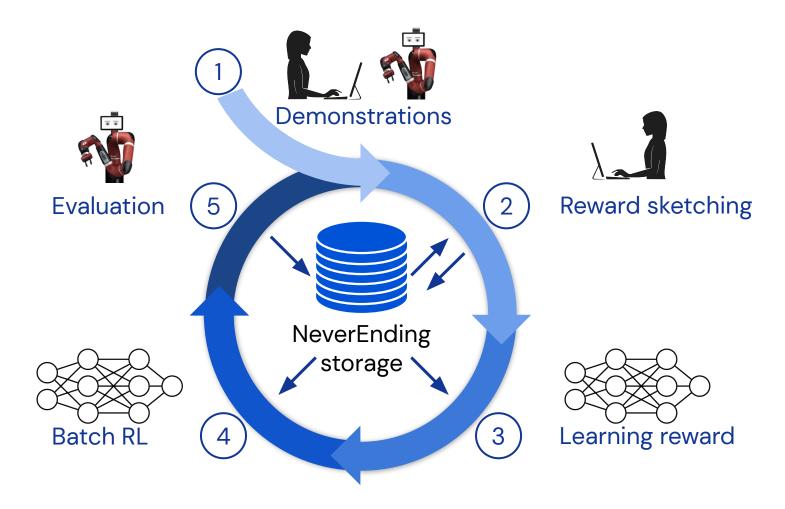
Example: Data Driven Robotics



A framework for data-driven robotics

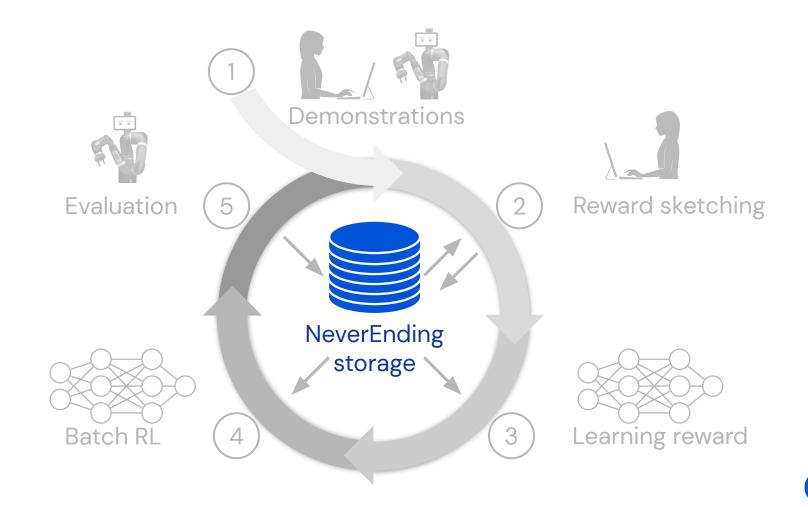


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(Serkan Cabi et al., 2019)

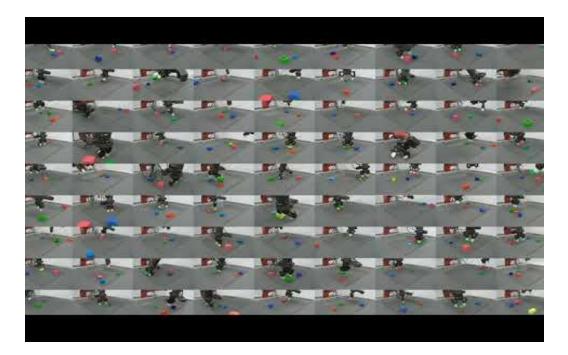




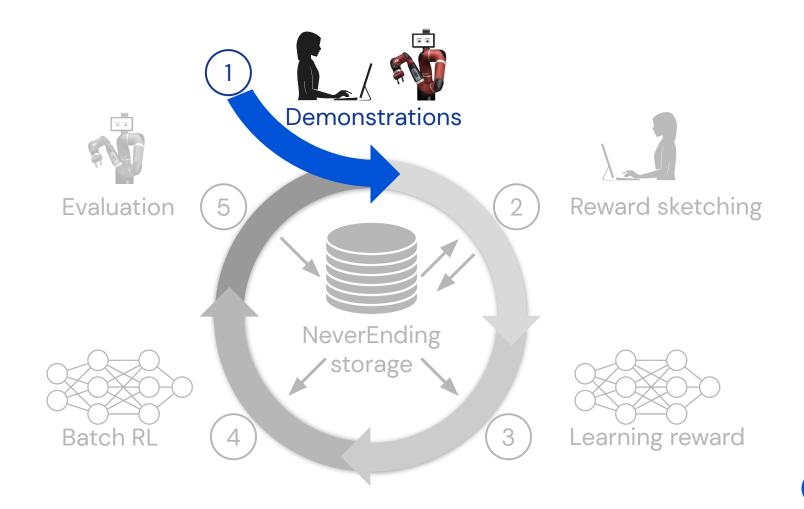
NeverEnding storage

Contains:

- Different tasks
- Demonstrations
- Agents
- Random policies
- Failed experiments etc.







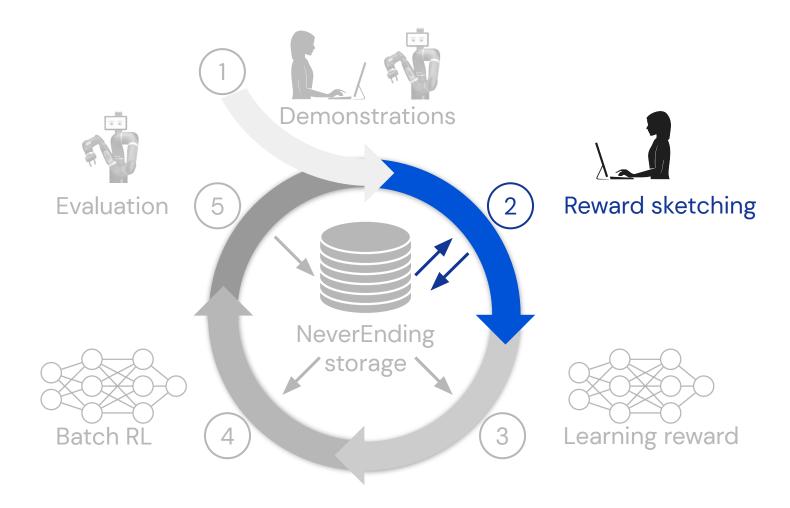


Step 1: Demonstrations









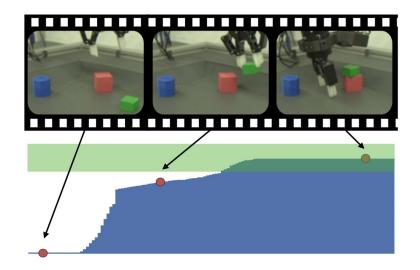


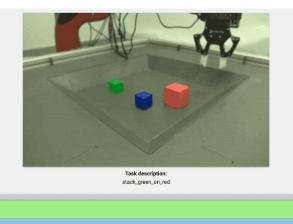
(Serkan Cabi et al., 2019)

Step 2: reward sketching



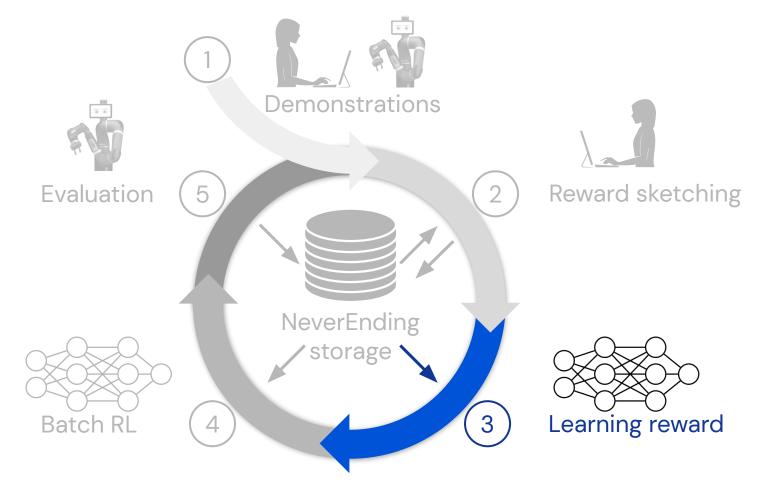
Task: stack green on red





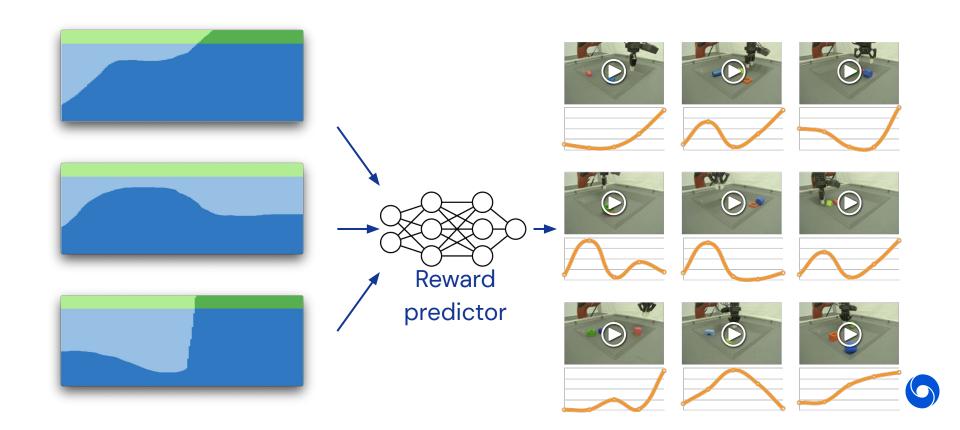


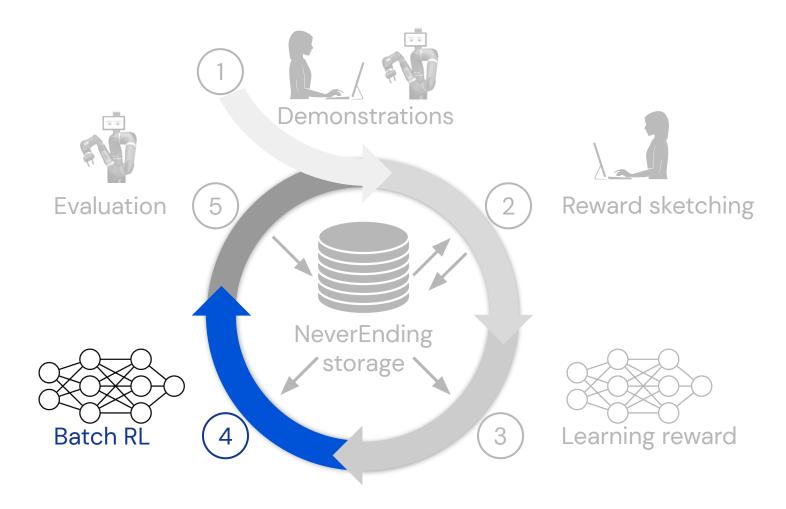




Serkan Cabi et al 2019

Learning reward



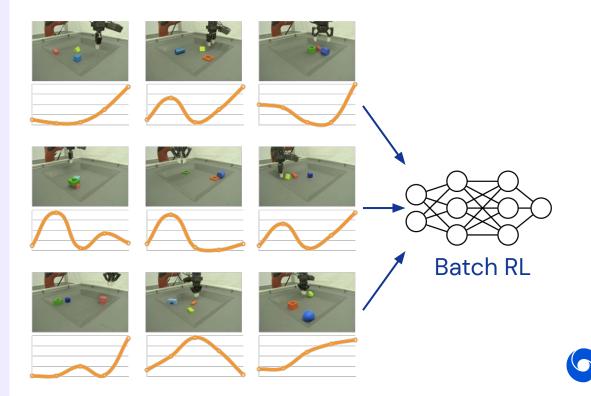


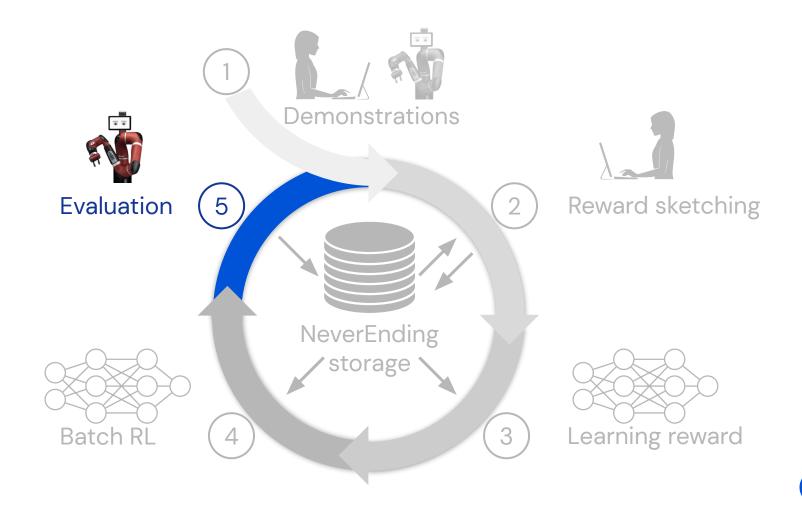
Serkan Cabi et al 2019

Batch RL

Agent:

- D4PG
- Recurrent state
- Uses demonstrations
- 25% task specific in each batch
- Diverse data is crucial!

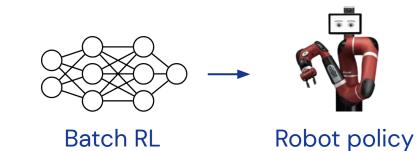




(Serkan Cabi et al., 2019)

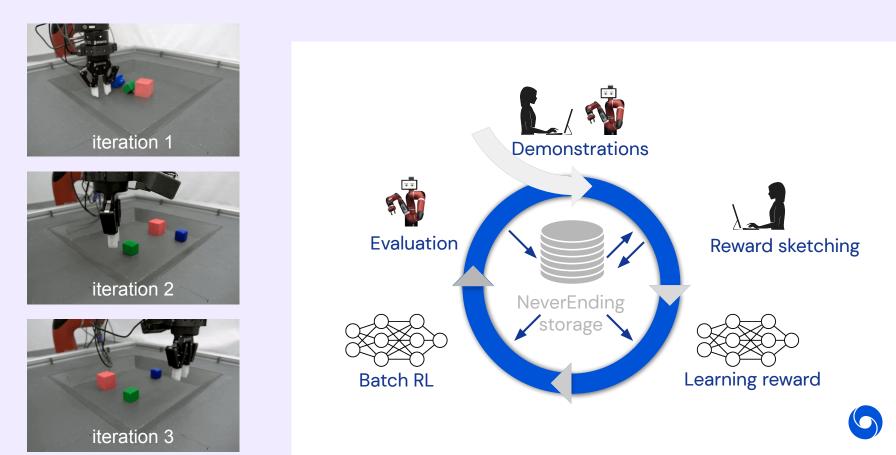
Evaluation

- Execute policy on the robot
- Record episodes to NeverEnding storage

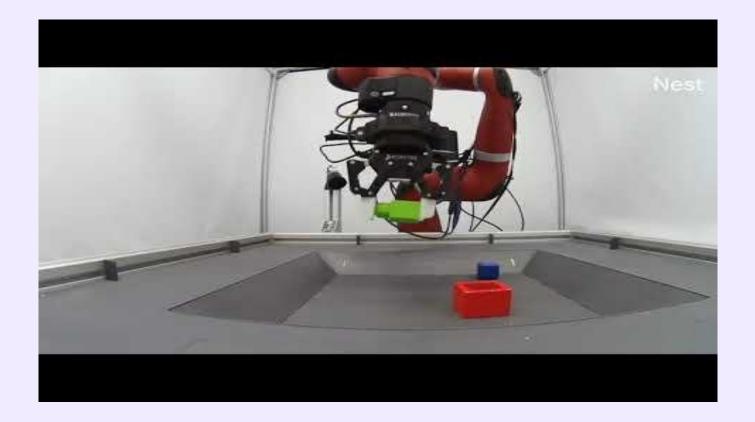




Iterative improvement



Adversarial robustness

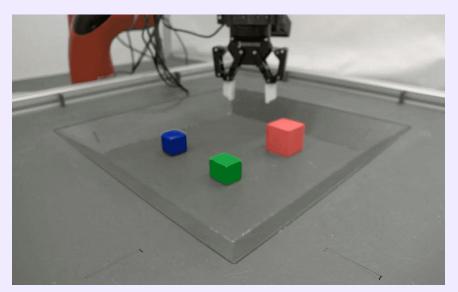




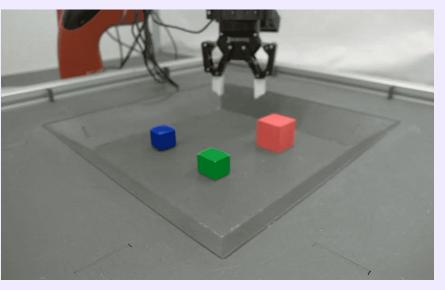


Better than human teleoperators





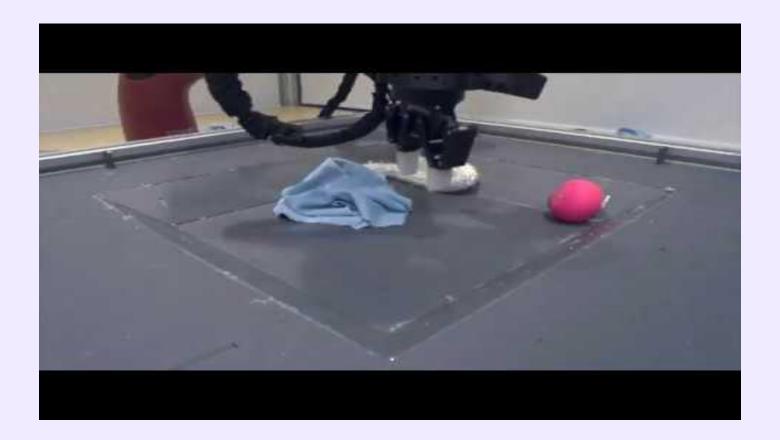






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Handles non-scriptable objects





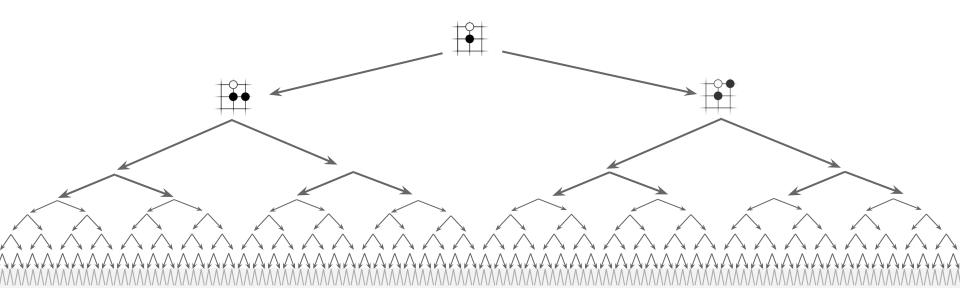
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Example: From AlphaZero to GNNs



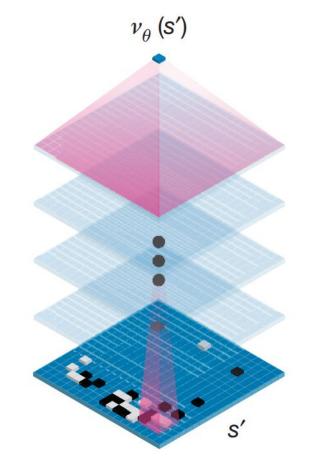


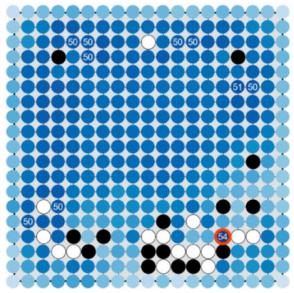
Exhaustive search





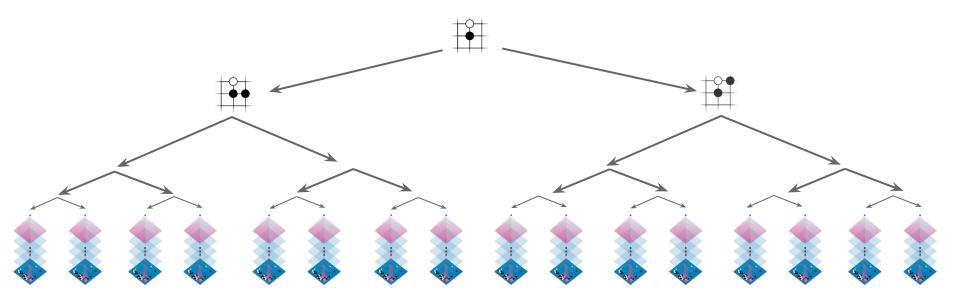
Value network







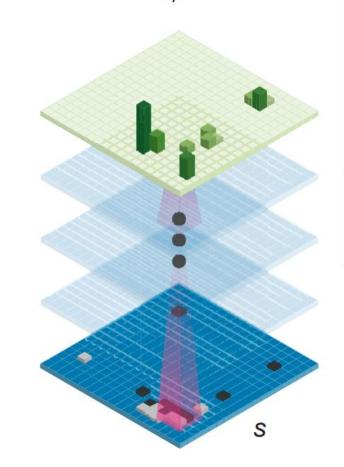
Reduce depth with value network

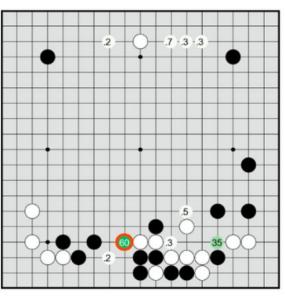




Policy network

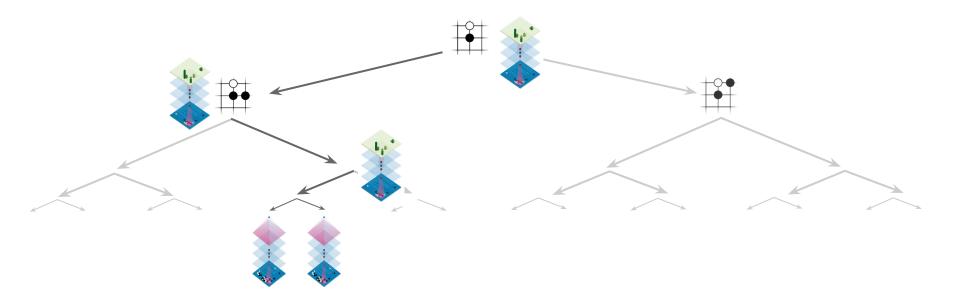
 $p_{\sigma \mid \rho}$ (as)



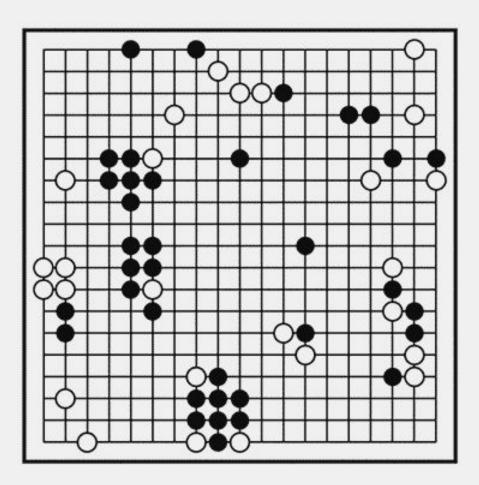




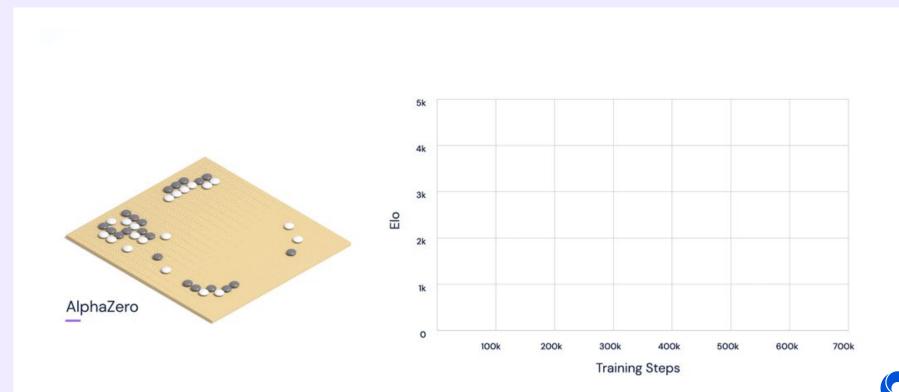
Reduce breadth with policy network





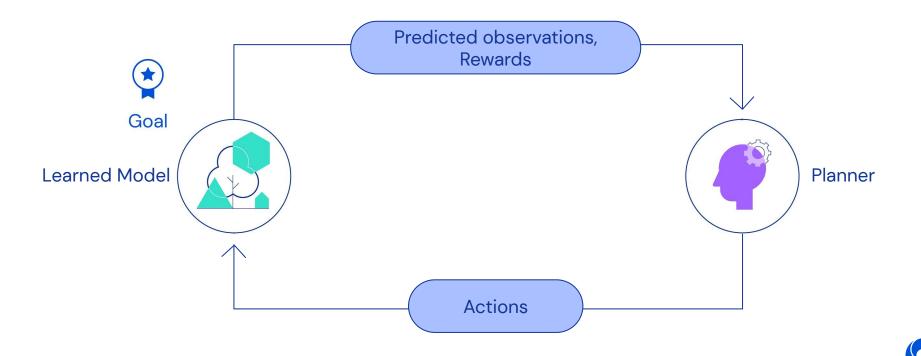


AlphaZero: Learning without Human Knowledge



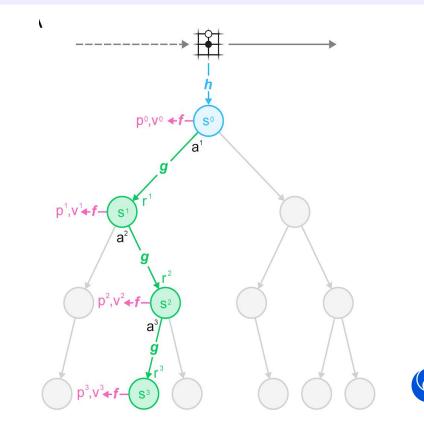
Model Based Reinforcement learning

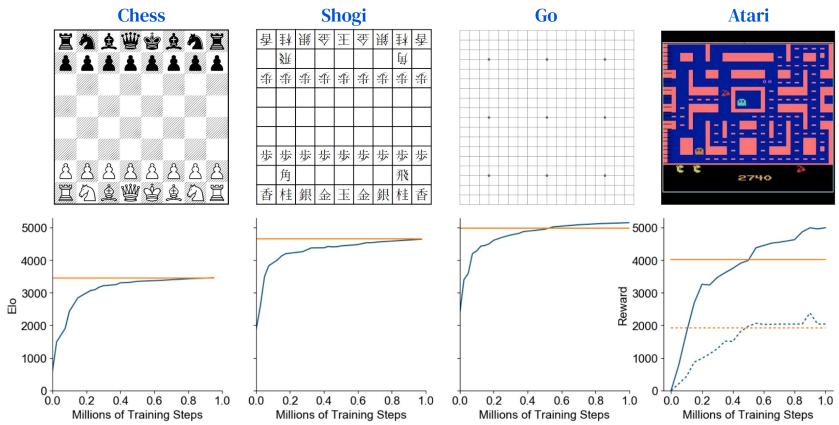
Making good decisions by learning about the world from experience and through imagination



MuZero: Planning with a Learned Model

representation
$$s^{O} = h_{\theta}(o_{\gamma} ..., o_{t})$$
prediction $\mathbf{p}^{k}, v^{k} = f_{\theta}(s^{k})$ dynamics $r^{k}, s^{k} = g_{\theta}(s^{k-1}, a^{k})$





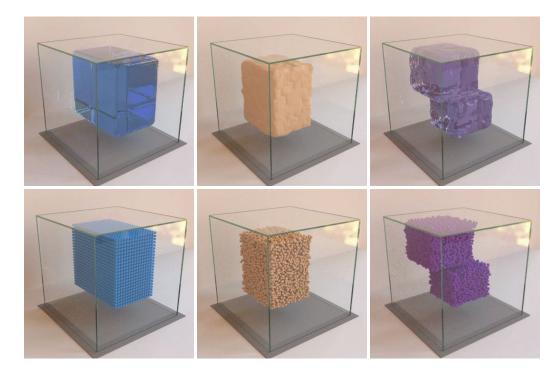
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Learning Models of the World

Graph Neural Networks

A general approach to learning simulations.

Single GraphNets architecture with single set of hyperparameters



Learning to Simulate Complex Physics with Graph Networks <u>https://arxiv.org/abs/2002.09405</u>



Model Framework

ENCODER PROCESSOR DECODER GN^M GN^1 $\cdots G^{M-1}$ G^0 \dot{q}^1 G^M YX-Construct graph Extract dynamics info Pass messages m+1C \mathbf{v}_i^M

Encoder

- Input features:
 - Position
 - Previous 5 velocities
 - Particle type
- Embed features with MLPs
- Construct neighbourhood graph

Processor

- Message-passing steps (many)
 - Edge function: MLP
 - Node function: MLP

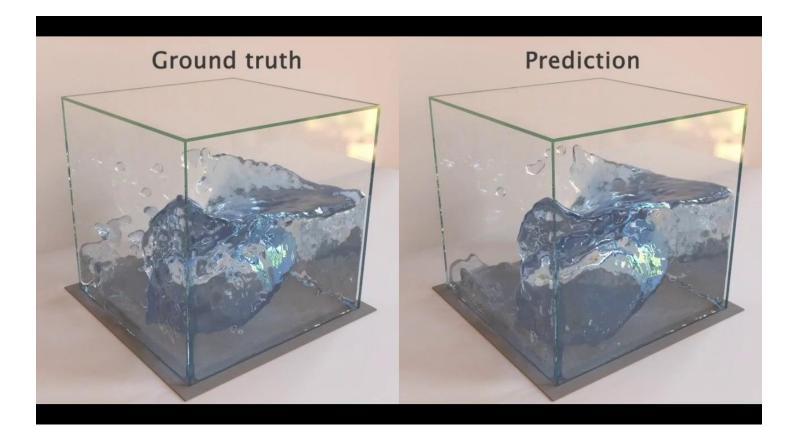
Decoder

- Extract acceleration
- Feed into Euler integrator



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Results: Rollout over 1000 steps (initialized from timestep=0) Private & Confidential





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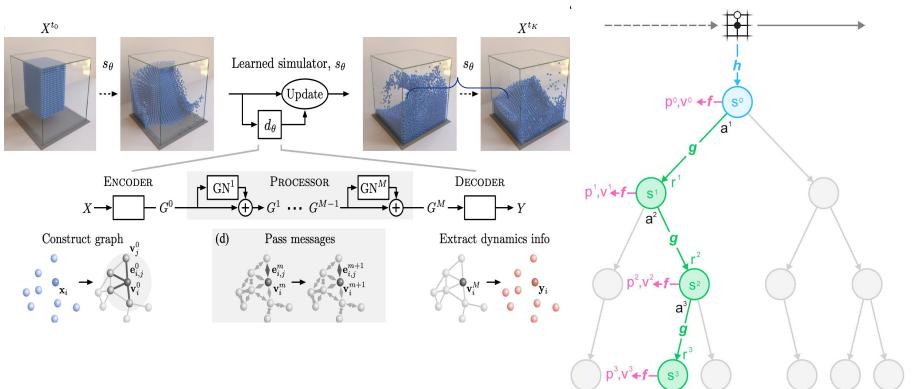
We need huge amounts of compute.

But why?



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Algorithmic Scaling





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The Bitter Lesson

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The biggest lesson that can be read from 70 years of AI research is that general methods that leverage computation are ultimately the most effective, and by a large margin.

Rich Sutton

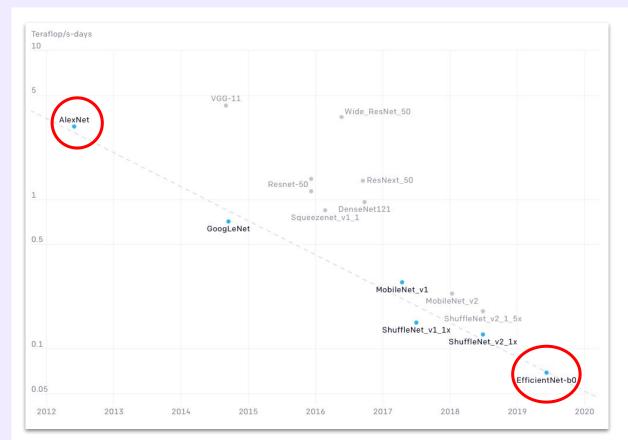
Unreasonable effectiveness of computational scaling

- Larger networks generalize better
- Larger networks train faster
- More Search and Planning is better

Algorithms catch up (eventually) but the gap is large.



Algorithmic efficiency gains aren't enough



Demonstrating **44x less compute** required to achieve AlexNet*-level performance over 7 years.

This is huge. But it's not enough

https://arxiv.org/abs/2005.04305



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During the same period, compute demand increased $>300,000x^{Private & Confidential}$



https://openai.com/blog/ai-and-compute/



How have we met the demand gap?

Petaflop/s-days 1e+4 AlphaGoZero • 1e+3 lOx/year AlphaZ 1e+2 Neural Machine Translation Neural hitecture 1e+1 TI7 Dota 1v1 Xception 1e+0 \$\$\$ meeting this gap DeepSpeech2 VGG 1e-1 ResNets ~10,000X Funding! Visualizing an GoogleNet Understandi 1e-2 AlexNet **AI HW** Dropout 1e-3 4-month doubling 1e-4 ~10x Moore's Law, CPUs DON 2012 2013 2014 2015 2016 2017 2018

AlexNet to AlphaGo Zero: A 300,000x Increase in Compute (Log Scale)

https://openai.com/blog/ai-and-compute/

If growth holds then this isn't sustainable long-term

Absolute limit is far off

- >£1tn/yr in HW budget
- Build out will continue
- Sufficient economic incentive
- Great progress still with small models in production

Impacts may be seen soon

- Potential inaccessibility of certain research to certain groups
- Inability to progress certain types of research
- Lack of investment in understanding auxiliary impact

HW Enabler of Al Research

- Increasingly harder problems
- Research peeking into future of products
- Diversity of HW catalyzes new research ideas



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Three Opportunities on The Road Ahead





Systems >> Chips

Research At Scale Is About People

- Hundreds of Researchers
- Thousands of Experiments
- Millions of Program/Meta parameters
- → Infinite Backlog
- 99% Throwaway!

Researchers
Experiments
Populations
HyperParameters
Controllers
Distributed Systems / Data
Math
Compilers
Runtimes / VMs
Networks / Systems
ML HW



Research Process as a System

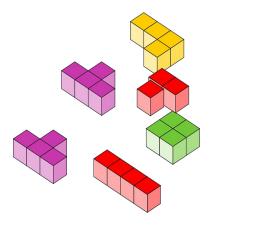
Optimize for Throughput in the whole system

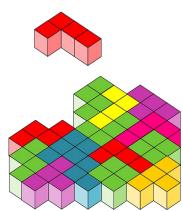
Experiment Manager:

- complete view of past, present, future
- total control of scheduling and placement

Results:

• 2X improvement in overall Utilization







Sustained Performance / TCO

→ Maximize Utility:

performed work / (cost of equipment + energy)

Most optimized research experiments: 70%

- Average research fleet wide peak FLOPs Utilization: 20%
- How could this be?

Sustained perf/\$ is not Peak Device perf/\$

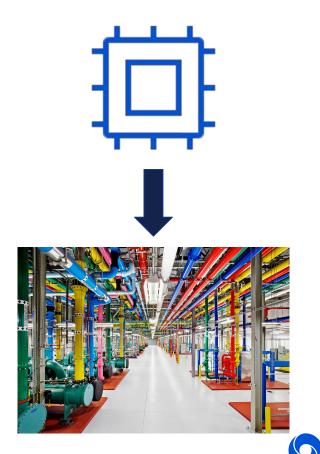
- Single Device Performance/Cost a weak predictor of overall Perf/\$
- Possible reasons for low Utilization:
 - 🔘 Startup time
 - Memory bound
 - I/O starvation
 - Compiler issues
 - Contention
 - 🔘 Runtime
 - 🔘 Amdahl's Monster



What to optimize for?

Utilization is independent of Chip parameters

- Ohip performance does not matter
- What matters is sustained system perf/\$
- More opportunities to design top down from DataCenter scale



Which Great Ideas for Warehouse scale Computer?







MOORE'S LAW



PREDICTION



HIERARCHY

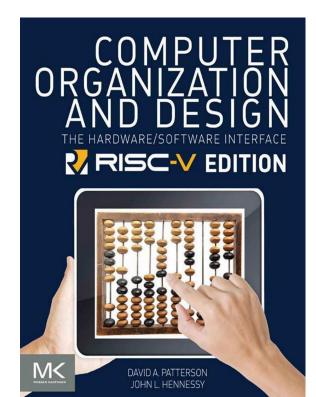




COMMON CASE FAST











What to do when there isn't plenty of room at the bottom?



(Not so) Plenty of Room at the Bottom

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I don't know how to do this on a small scale in a practical way, but I do know that computing machines are very large; the fill rooms. Why can't we make them very small, make them of little wires, little elements – and by little I mean little. For instance, the wires should be 10 or 100 atoms in diameter, and the circuits should be a few thousand angstroms across.

Richard Feynman, 1959

What does a warehouse scale computer look like when we've reached The Bottom?





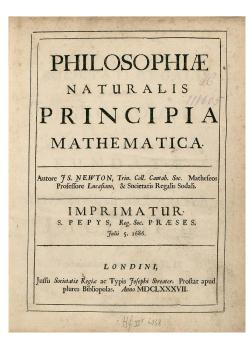
What actually matters?

\rightarrow From first principles:

) Si

O Power

Software

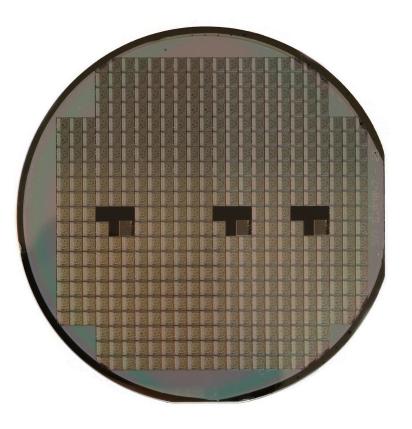


How far away are we from reaching the bottom?



True cost of operations

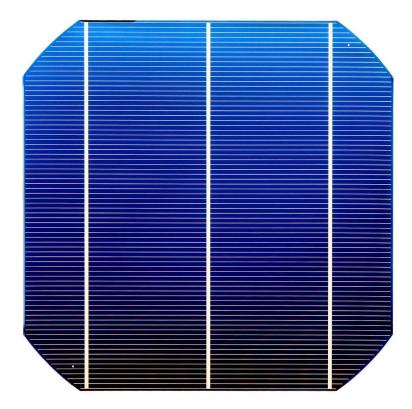
- What if the WSC was built just out of pure Si?
 - 1000X improvement in perf/\$





True cost of power

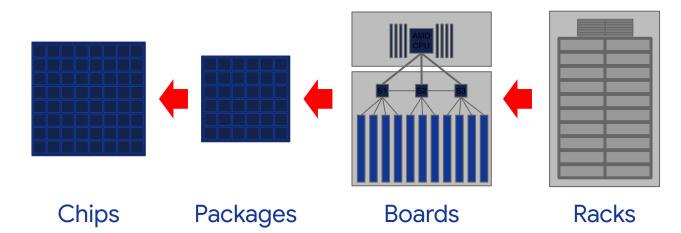
- Surprise: power is still Si (until fusion?)
- True cost of power:
 - 1000X more power/\$from pure Si + Sun





50 year trends

- Omponents disappear into Si
- Make up volume with more computers





How do we bridge the 1000X gap?

- Move everything into Si (even if it costs more)
- Only add components to the system that are required
- Exploit non-linearities in system properties
 - Number of distinct components
 - Rate of defects per area
 - 3D stacking
 - Massless cooling
 - Automation



Paradigm Shift

Producing an electric car is not just replacing the combustion engine with an electric motor!



New rules for system design?

OLD

- O Big complex chips
- O Bespoke, expensive
- Few
- Fancy, reliable
- Control decisions in HW

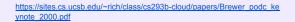
NEW

- Smaller simple chips
- Few components, all in Si
- Huge quantities
- Cheap, less reliable
- Control decision in software



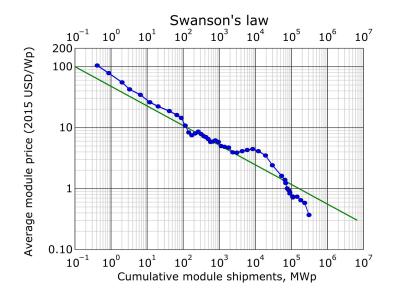
You think this is impossible?







Simple designs are easy to scale up





Solar volume 1000X larger now than compute Silicon!





Where is the Software floor?

Back to the 90's

Cluster-Based Scalable Network Services

Armando Fox Steven D. Gribble University of California at Berkeley

Yatin Chawathe

Eric A. Brewer Paul Gauthier

Inktomi Corporation

- By 2000 we learned how to run disaggregated services over thousands of machines.
- Machine Learning systems also run over thousands of Devices.
- Our software infrastructure stack had to increase in complexity to accommodate.

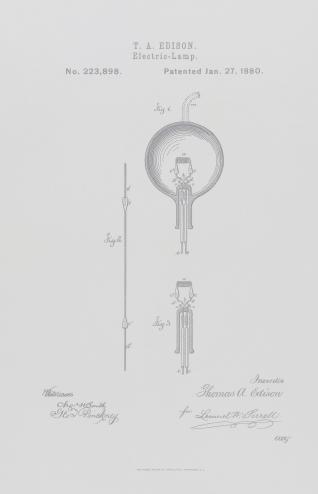


Where do performance/\$ gains come from?

Big gains come from Paradigm Shifts
CPU -> GPU -> TPU

- Process Node improvements are a wave we all ride on.
 - Put other way:

Major performance improvements can only come at the expense of creating significantly different systems.





Where do performance/\$ gains come from?

Gains from Paradigm Shifts are eroded due to complexity and inflexibility of the infrastructructure stack.

Is there a solution?



What does diversity of HW matter?

Diversity of HW gives rise to different research directions and patterns of thought but is too expensive to sustain due to high per-platform software overheads.

Is there a solution?

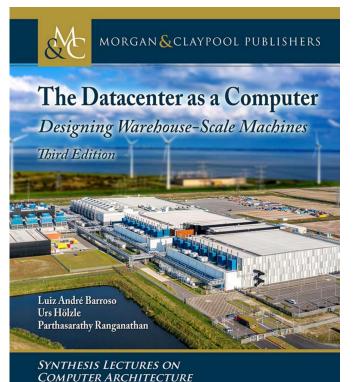


This time it's different?

- Properties of AI Programs:
 - Highly regular
 - Constrained by dataflow
 - Constrained by arithmetic
 - Low amount of data dependent control flow
 - >µs timescales for basic operations
- Coarseness & Timescales allow for distributed programs with software control.

This time it's different?

- Warehouse scale programs on Warehouse scale computers?
- What does this mean for compilers and runtimes?
- Opportunity to redo 70s, 80s and 90s research in systems and PL?



largaret Martonosi, Series Editor

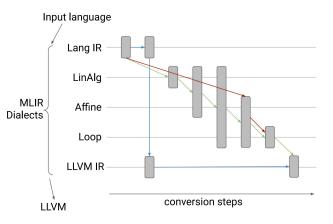


This time it is different.

- Optimization during compilation is about choices.
- We are very good at making choices now:
 - Large scale RLGenerative models
- What will this buy us?

MLIR Code Generation Flows

Tentative, Alex Zinenko's snapshot





This time it is different.

- Radical slimming down of code base:
 - Multi-Purpose solutions
 - List of choices
 - Engine to decide which ones to make

 \rightarrow

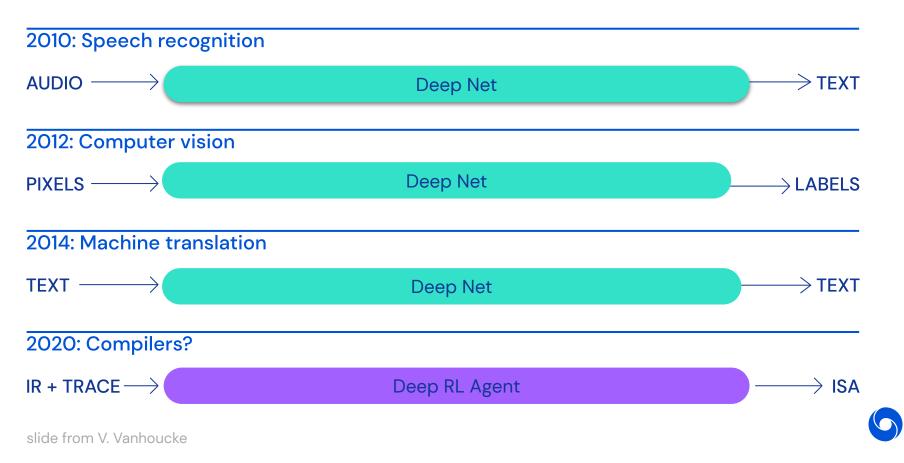
Greater adaptability. Easier to take advantage of paradigm shifts.

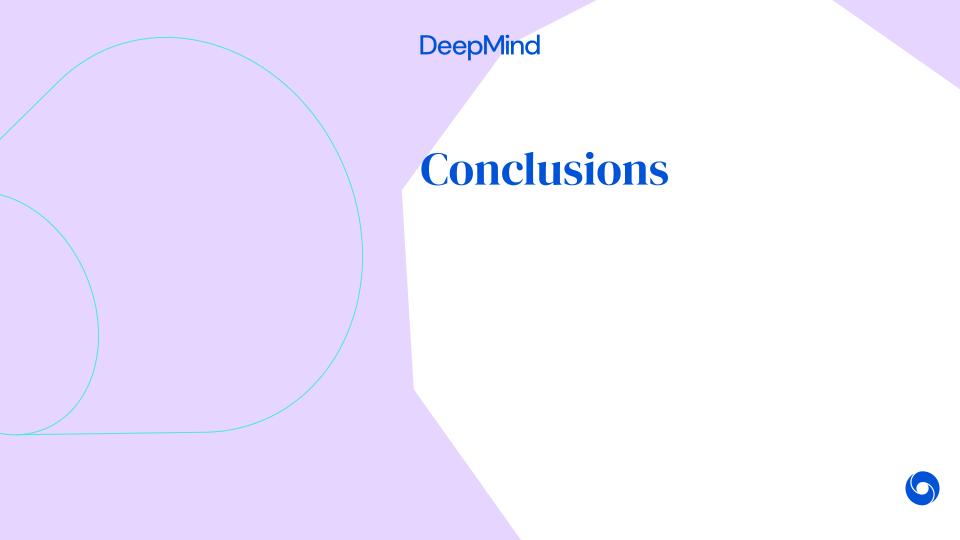
 \rightarrow

Leading to greater research velocity.



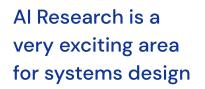
Deep Learning for compilers ... the time has come?





This time it could really be different!

New epoch in computing is bringing in new opportunities.



It is time start focusing on whole systems It is time to exploit economies of scale for HW

It is time to embrace advances in ML for the design of software infrastructure

